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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				101 618146	
(use as many sheets as necessary)				Filing Date	July 11, 2003
Sheet	1	of	2	First Named Inventor	Warren E. Cory
				Art Unit	Unknown
				Examiner Name	Unknown
				Attorney Docket Number	X-1065-1C US

## **U.S. PATENT DOCUMENTS**

## **FOREIGN PATENT DOCUMENTS**

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<b>Complete If Known</b>			
<b>Application / Conf. N.</b>		Unknown <i>101618196</i>	
<b>Filing Date</b>		July 11, 2003	
<b>First Name Inventor</b>		Warren E. Cory	
<b>Art Unit</b>		Unknown	
<b>Examiner Name</b>		Unknown	
<b>Attorney Docket Number</b>		X-1065-1C US	
<b>Sheet</b>	2	<b>of</b>	2

## **OTHER – NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
<i>me</i>		Cary D. Snyder and Max Baron; "Xilinx's A-to-Z System Platform"; Cahners Microprocessor; The Insider's Guide to Microprocessor Hardware; Microdesign Resources; February 6, 2001; pp. 1-5.
<i>me</i>		Xilinx, Inc.; "Virtex-II Platform FPGA Handbook"; published December 2000, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 33-75.

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